IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Tsuyoshi YAMAMOTO, et al.

Serial No.: Not Yet Assigned

Filed: February 12, 2002

For: METHOD OF MOUNTING ELECTRONIC COMPONENT ON SUBSTRATE WITHOUT GENERATION OF VOIDS IN BONDING MATERIAL

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

February 12, 2002

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

Copies of the references are enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 01-2340.

> Respectfully submitted, ARMSTRONG, WESTERMAN & HATTORI. LLP

Atty. Docket No.: 020154

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Enclosures: PTO-1449; References (2)

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INFORMATION
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STATEMENT
PTO-1449

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Serial No. New Appln.

Applicant(s): Tsuyoshi YAMAMOTO, et al.

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Group Art Unit:

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
<u> </u>	AA	5,878,942	Kodama et al.	03/09/99			
	AB						
	AC						
	AD						
	AE						

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Translation (Yes or No)
 AF	9-92682	04/04/97	Japan	Abstract
 AG				
 AH				
 AI				
 AJ				

OTHER DOCUMENTS

Examiner	AL	Date Considered
	A T	
	AK	